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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/724,211	12/01/2003	Takashi Miyazawa	117783	9841
25944	7590	12/12/2006	EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			CHOW, DOON Y	
			ART UNIT	PAPER NUMBER
			2629	

DATE MAILED: 12/12/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/724,211

Applicant(s)

MIYAZAWA, TAKASHI

Examiner

Dennis-Doon Chow

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 28 September 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-36 is/are pending in the application.
- 4a) Of the above claim(s) 22-35 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-17, 21 and 36 is/are rejected.
- 7) ☒ Claim(s) 18-20 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Applicant's election with traverse of Species I, Figure 3, claims 1-21 and 36 in the reply filed on September 28, 2006 is acknowledged. The traversal is on the ground(s) that the subject matter of all species is sufficiently related that a thorough search for the subject matter of any one species would encompass a search for the subject matter of the remaining species. Thus, the search and examination of the entire application could be made without serious burden. This is not found persuasive because it is not true that a search for the subject matter of any one species would encompass a search for the subject matter of the remaining species. A different search is clearly required for a different Species.

The requirement is still deemed proper and is therefore made FINAL.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1-17, 21 and 36 are rejected under 35 U.S.C. 102(e) as being anticipated by Yamagishi et al. (6501466).

Regarding to claims 1, 2, 5, 7, 8, 11, 13, 14, and 36, Yamagishi discloses a method of driving an electro-optical apparatus including n rows of scanning lines each including a first subscanning line and a second subscanning line (SCAN A and SCAN B, Figs. 1 and 5), m columns of data lines (DATA, Figs. 1 and 5), a power-supply line (Vdd, Fig. 1), and a plurality of unit circuits arranged in n rows and m columns in association with intersections of the scanning lines and the data lines, each of the plurality of unit circuits including a first transistor (TFT2, Fig. 1) having a first terminal and a second terminal, a capacitor (C, Fig. 1) coupled to a first control terminal of the first transistor, a second transistor (TFT4, Fig. 1) that controls the electrical connection between the first terminal and the capacitor, the second transistor having a third terminal and a fourth terminal, a third transistor (TFT3, Fig. 1) having a fifth terminal and a sixth terminal, and an electro-optical element (OLED, Fig. 1) connected to the first transistor; and a second control terminal of the second transistor being coupled to the second subscanning line of one of the n rows of scanning lines, a third control terminal of the third transistor is coupled to the first subscanning line of the one of the n rows of scanning lines, and the sixth terminal is connected to one of the m columns of data lines, the method comprising: a first step of accumulating a data signal supplied via one of the m columns of data lines in the capacitor as a charge while the second transistor and the third transistor are both on, and setting a conduction state of the first transistor according to the data signal; and a second step of turning off the third transistor and turning on the second transistor, and supplying an amount of charge that causes reduction in the

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conduction state, set in the first step, of the first transistor (col. 11, line 31 to col. 12, lines 10).

Regarding to claims 3, 9, and 15, Yamagishi further discloses the second terminal of the first transistor being electrically coupled to a predetermined potential, and a potential that is different from the predetermined potential being applied to the first control terminal in the second step (col. 11, line 31 to col. 12, lines 10).

Regarding to claims 4, 10 and 16, Yamagishi inherently discloses the potential applied to the first control terminal in the second step being a potential obtained by subtracting a threshold voltage of the first transistor from the predetermined potential or a potential obtained by adding the threshold voltage of the first transistor to the predetermined potential.

Regarding to claims 6, 12, and 17 Yamagishi further discloses, in the second step, the first transistor is turned off by the potential applied to the first control terminal, thereby stopping supply of a current to the electro-optical element (col. 11, line 31 to col. 12, lines 10).

Regarding to claims 21, the electro-optical elements inherently including three types of light-emitting elements so that red, green and blue color can be generated. Yamagishi further disclose each of the scanning lines is connected to one light-emitting element.

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4. Applicant cannot rely upon the foreign priority papers to overcome this rejection because a translation of said papers has not been made of record in accordance with 37 CFR 1.55. See MPEP § 201.15.

5. Claims 1, 7 and 13 are rejected under 35 U.S.C. 102(e) as being anticipated by Hiroshi (JP2003-216100).

Hiroshi discloses a method of driving an electro-optical apparatus including n rows of scanning lines each including a first subscanning line and a second subscanning line (17a2, 17c2, Figs. 117 and 118), m columns of data lines (18, Figs. 117 and 118), a power-supply line (17a1, Figs. 117 and 118), and a plurality of unit circuits arranged in n rows and m columns in association with intersections of the scanning lines and the data lines, each of the plurality of unit circuits including a first transistor (11a, Figs. 117 and 118) having a first terminal and a second terminal, a capacitor (19, Figs. 117 and 118) coupled to a first control terminal of the first transistor, a second transistor (11b, Figs. 117 and 118) that controls the electrical connection between the first terminal and the capacitor, the second transistor having a third terminal and a fourth terminal, a third transistor (11c, Figs. 117 and 118) having a fifth terminal and a sixth terminal, and an electro-optical element (15, Figs. 117 and 118) connected to the first transistor; and a second control terminal of the second transistor being coupled to the second subscanning line of one of the n rows of scanning lines, a third control terminal of the third transistor is coupled to the first subscanning line of the one of the n rows of scanning lines, and the sixth terminal is connected to one of the m

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columns of data lines, the method comprising: a first step of accumulating a data signal supplied via one of the m columns of data lines in the capacitor as a charge while the second transistor and the third transistor are both on, and setting a conduction state of the first transistor according to the data signal; and a second step of turning off the third transistor and turning on the second transistor, and supplying an amount of charge that causes reduction in the conduction state, set in the first step, of the first transistor.

Allowable Subject Matter

6. Claims 18-20 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

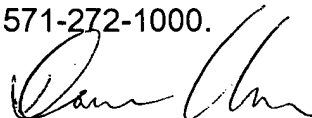
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dennis-Doon Chow whose telephone number is 571-272-7767. The examiner can normally be reached on 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on 571-272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.


Dennis-Doon Chow
Primary Examiner
Art Unit 2629

D. Chow
December 7, 2006